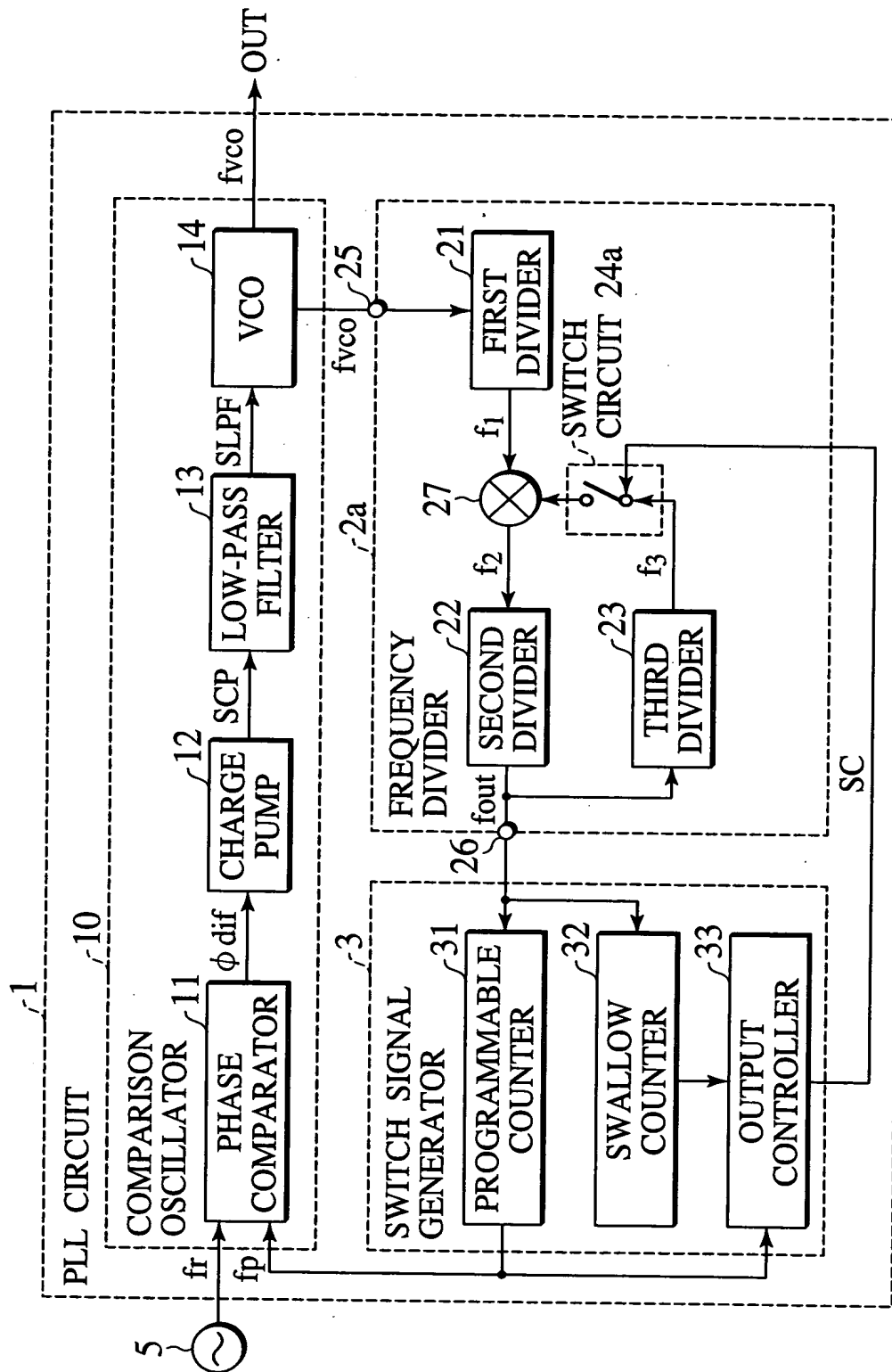


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FIG.1

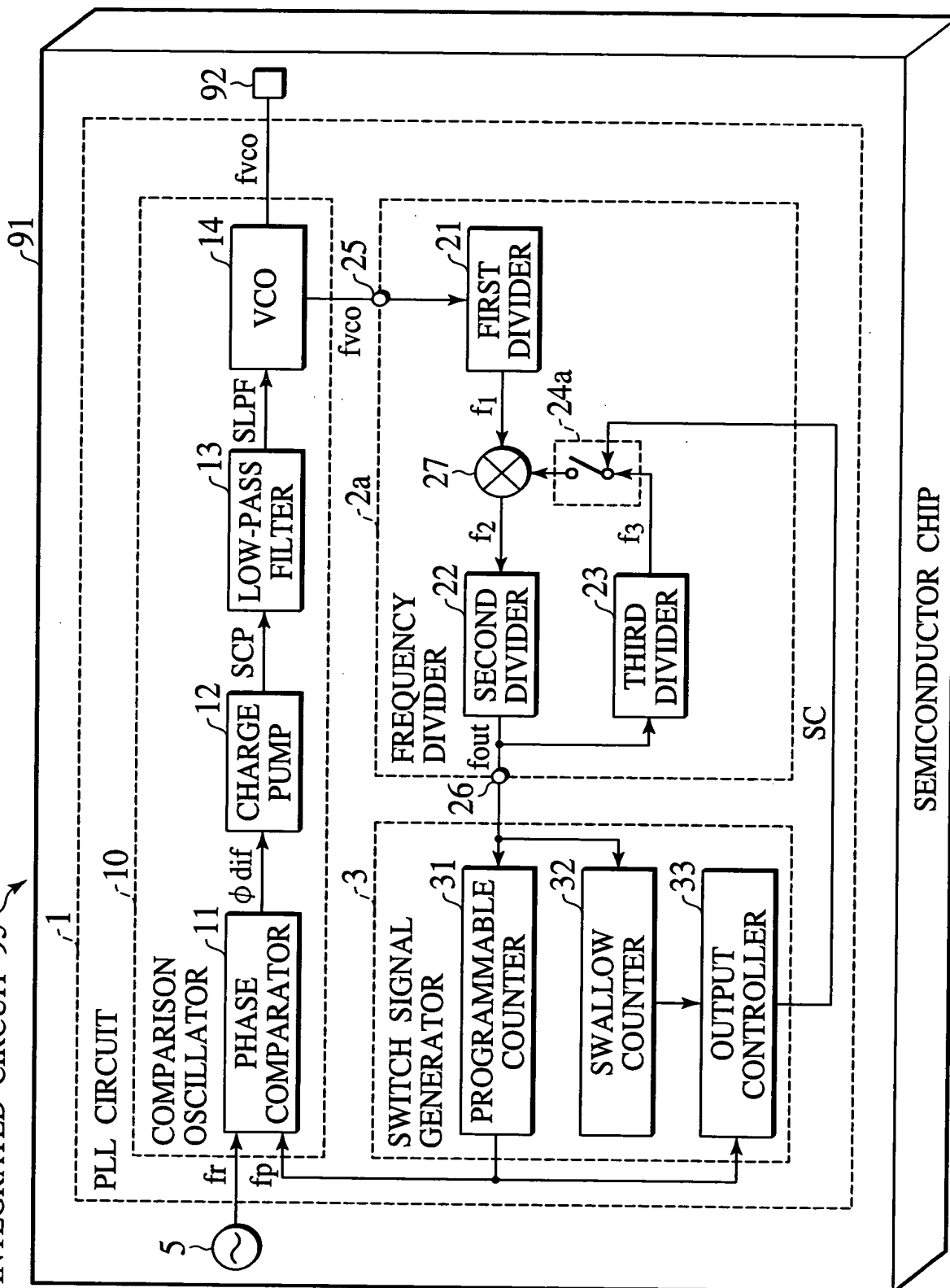


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FIG.2

SEMICONDUCTOR

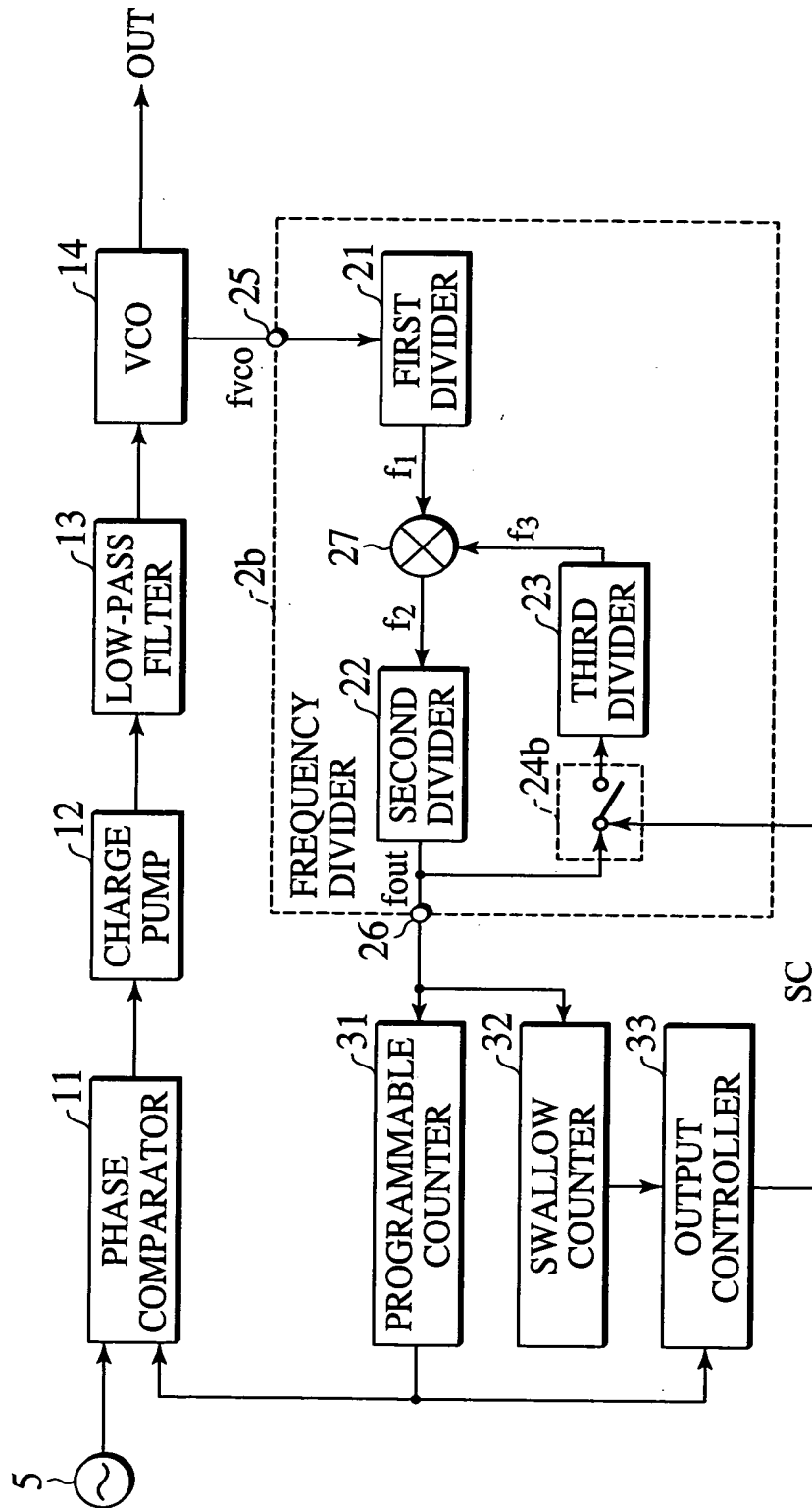
INTEGRATED CIRCUIT 95



SEMICONDUCTOR CHIP

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FIG.3



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FIG.5A

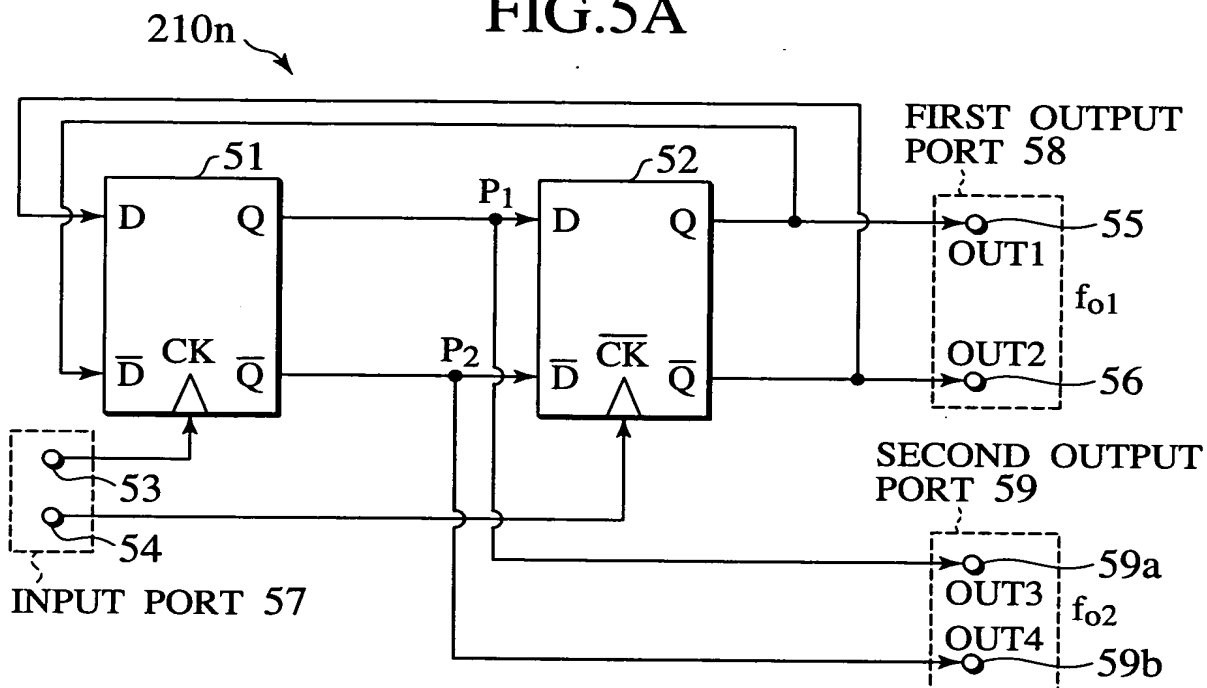
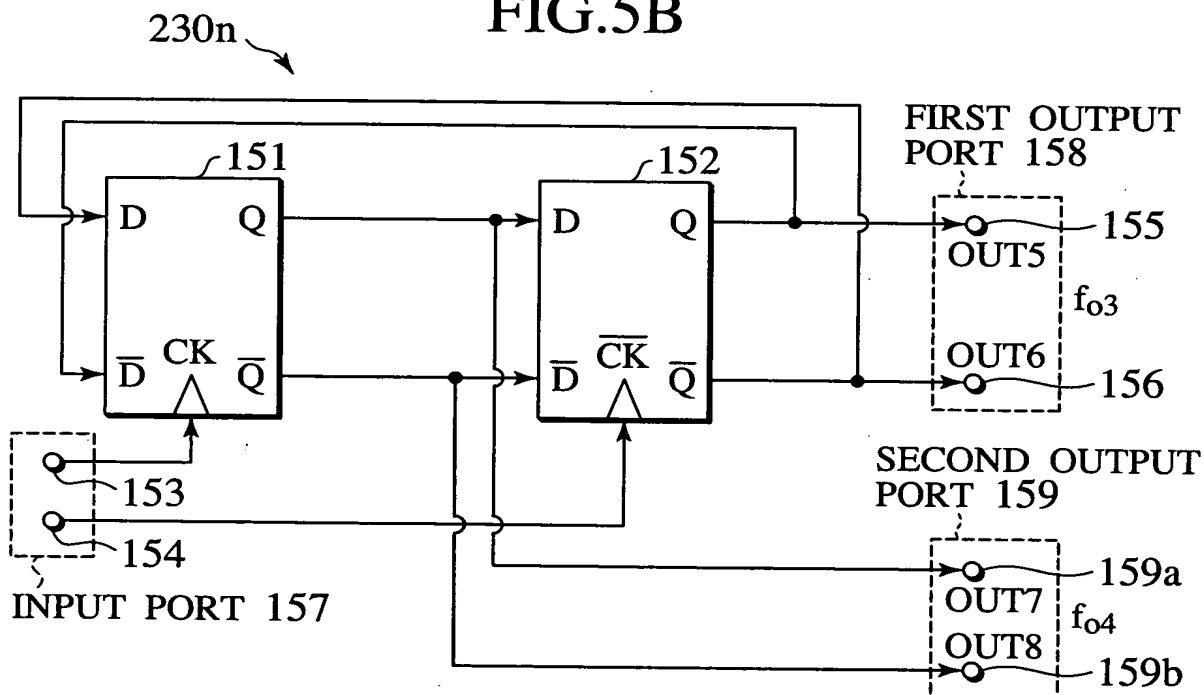
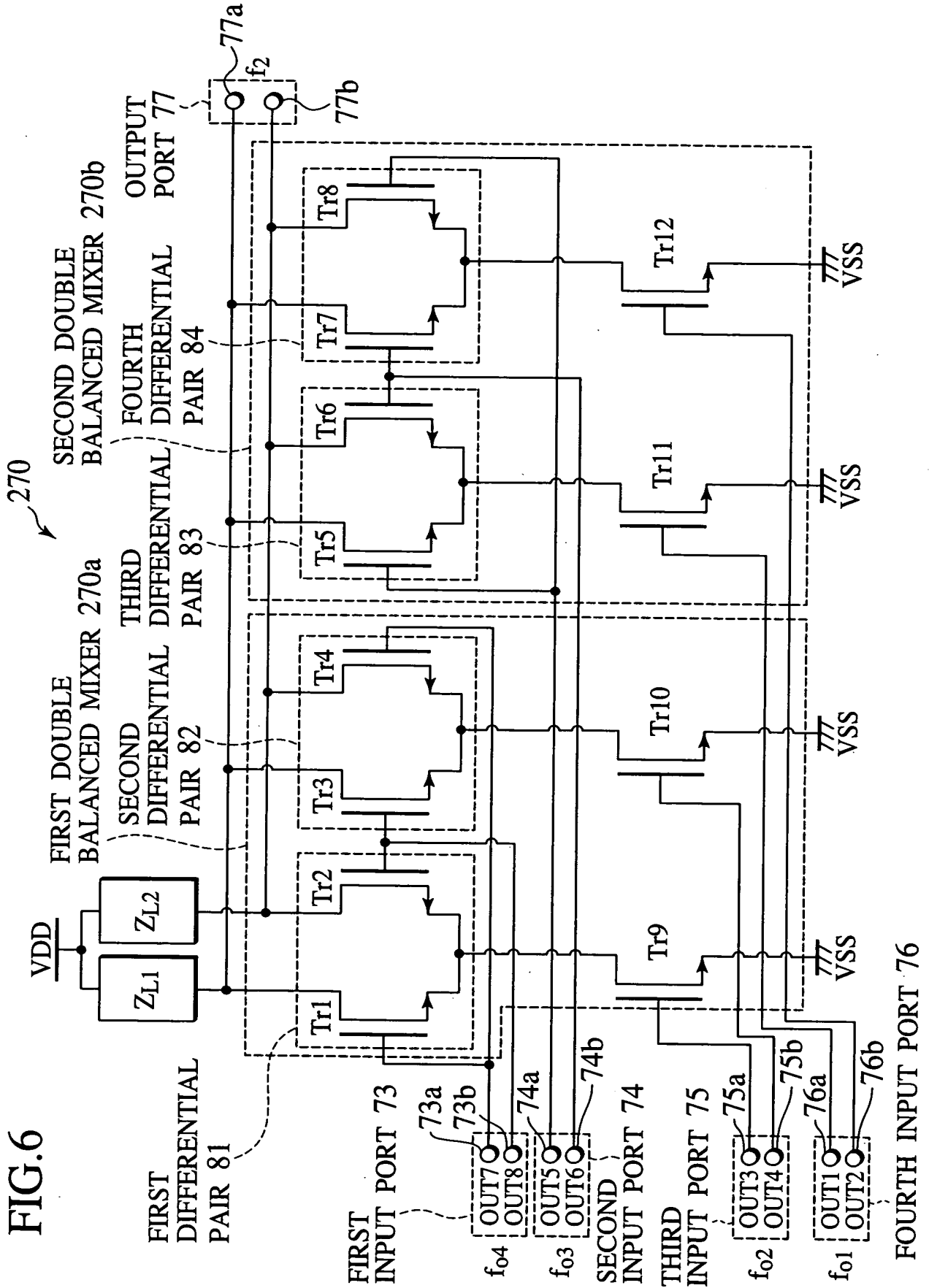


FIG.5B

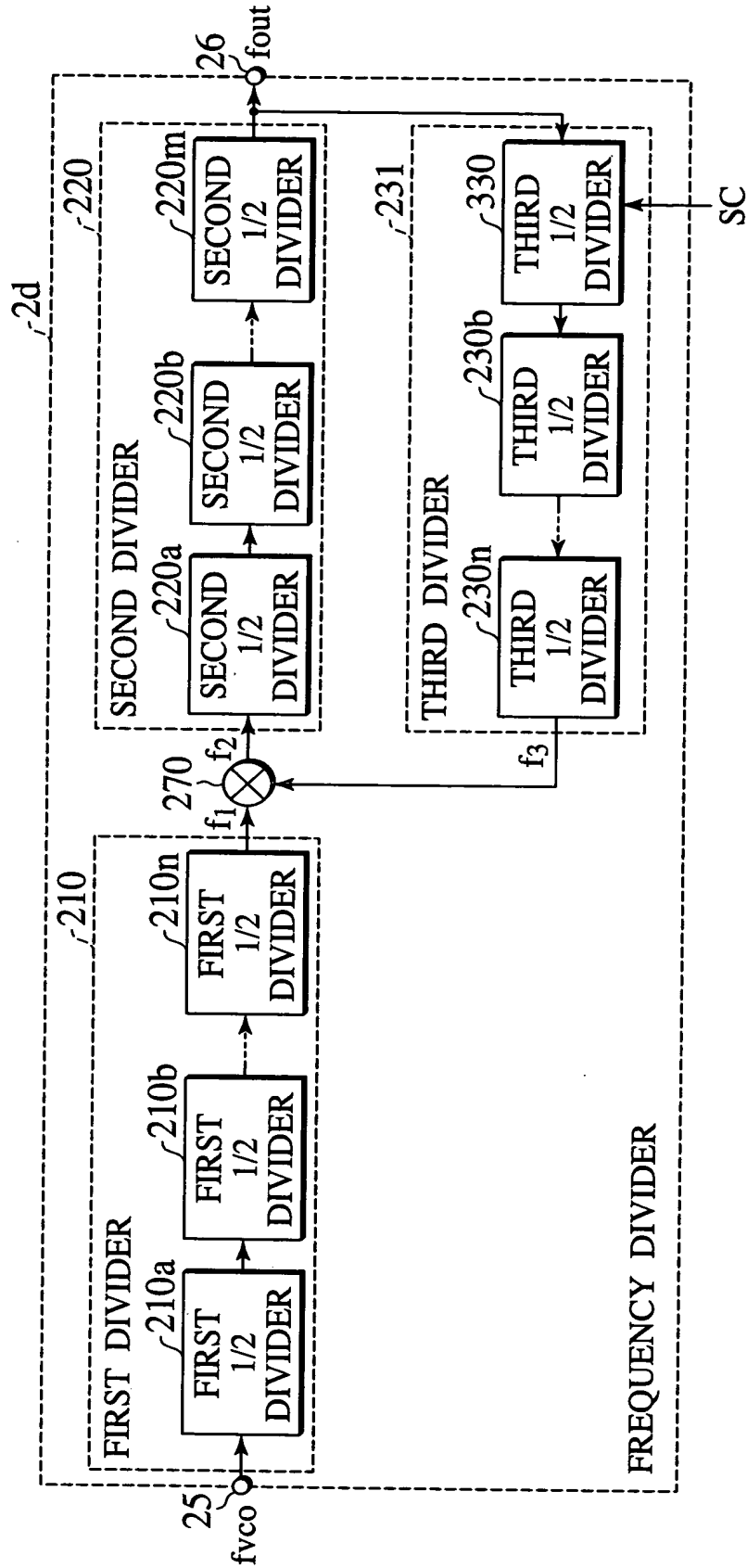


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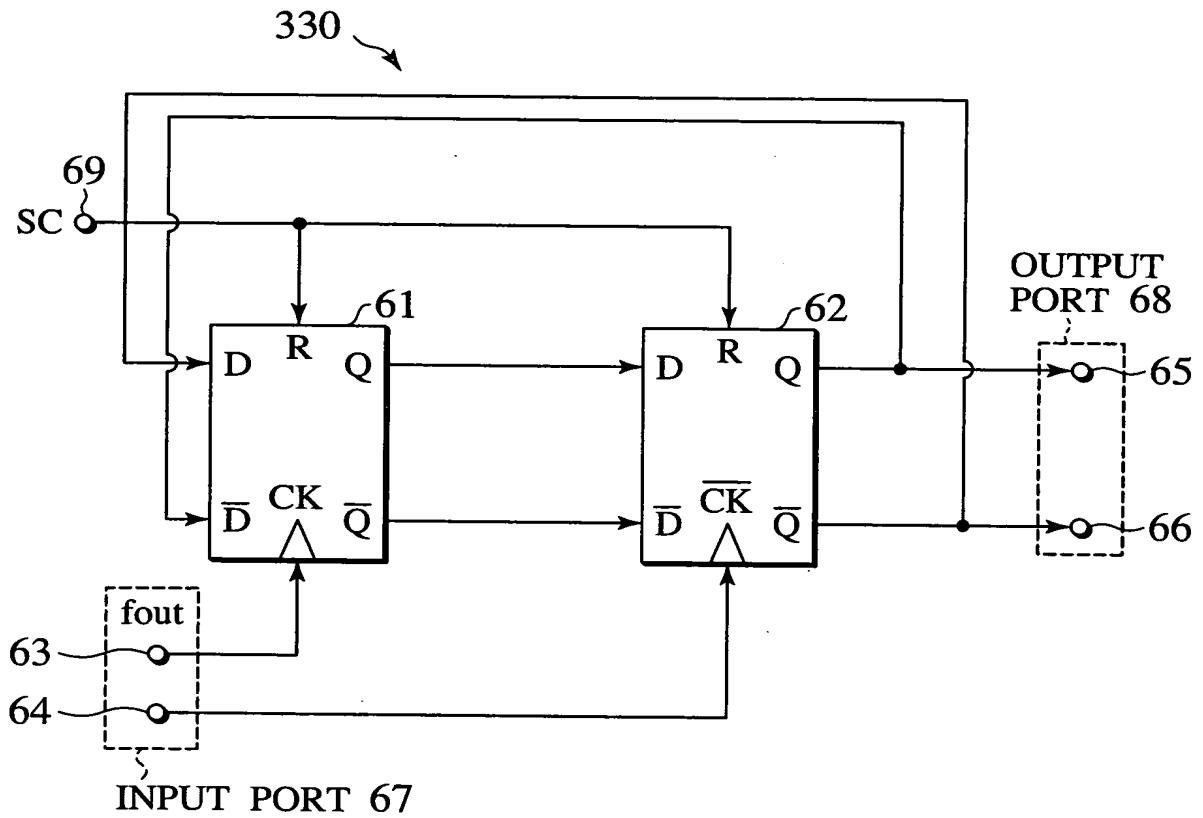
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FIG. 7



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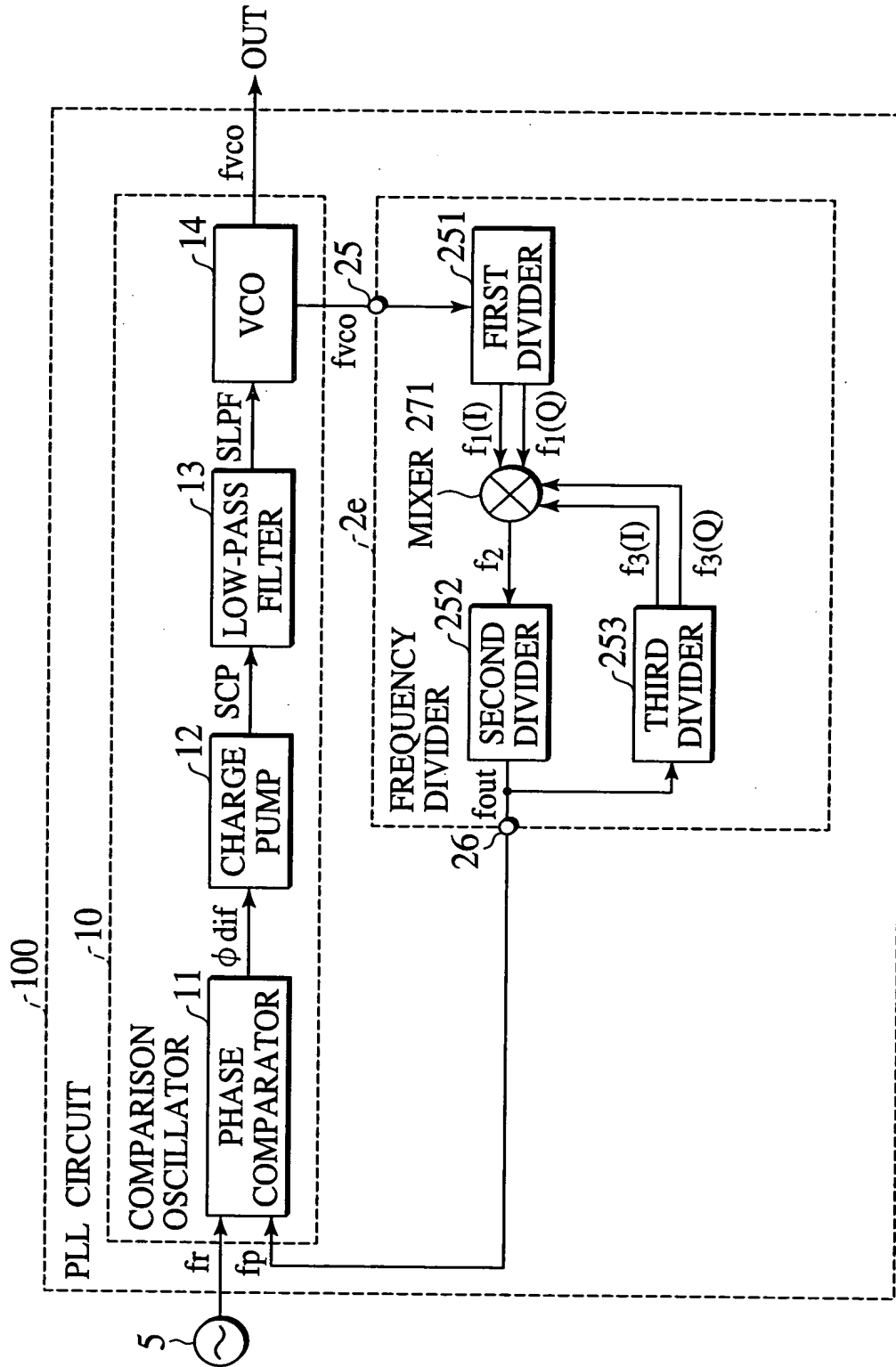
FIG.8



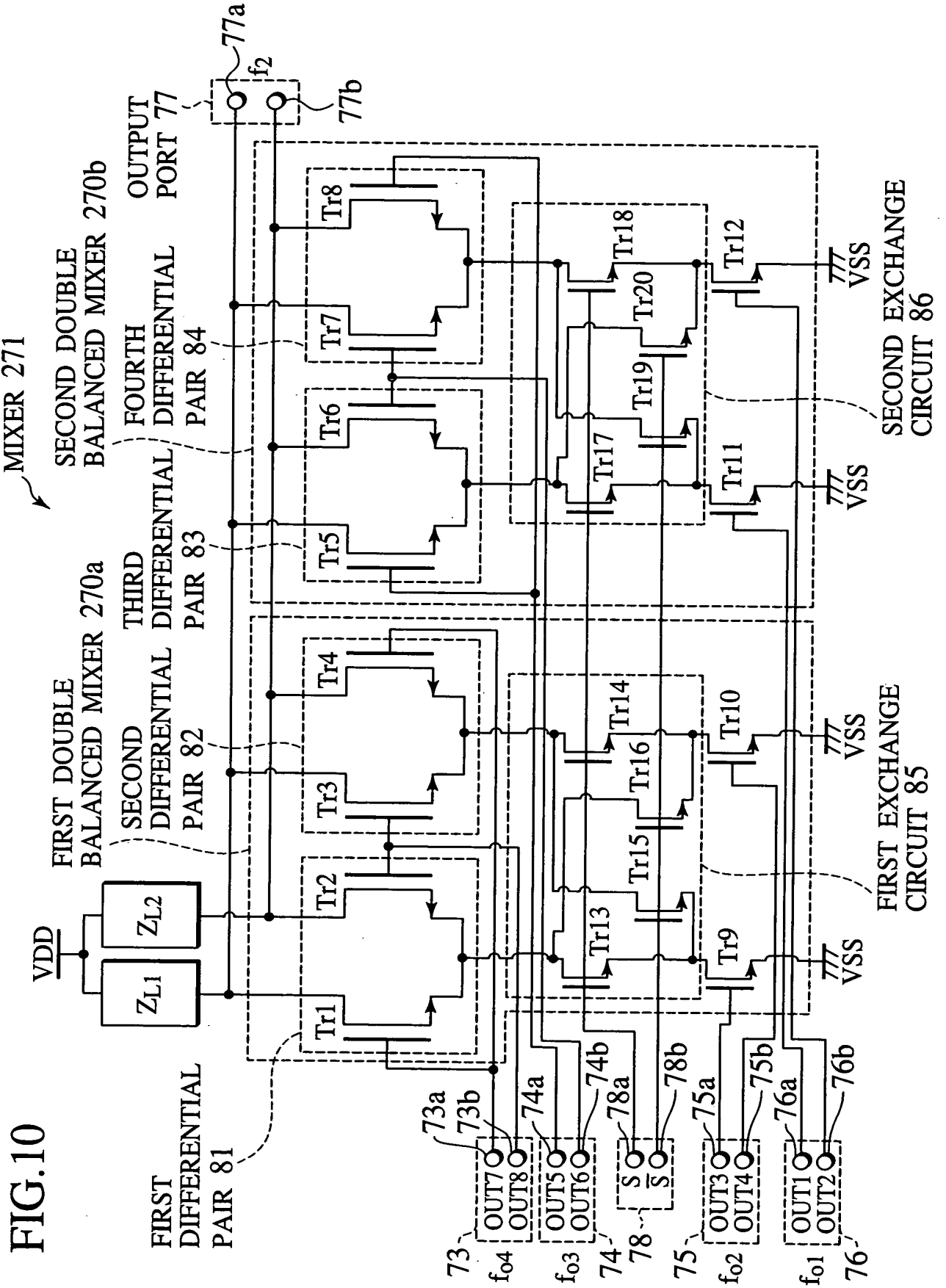


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FIG. 9

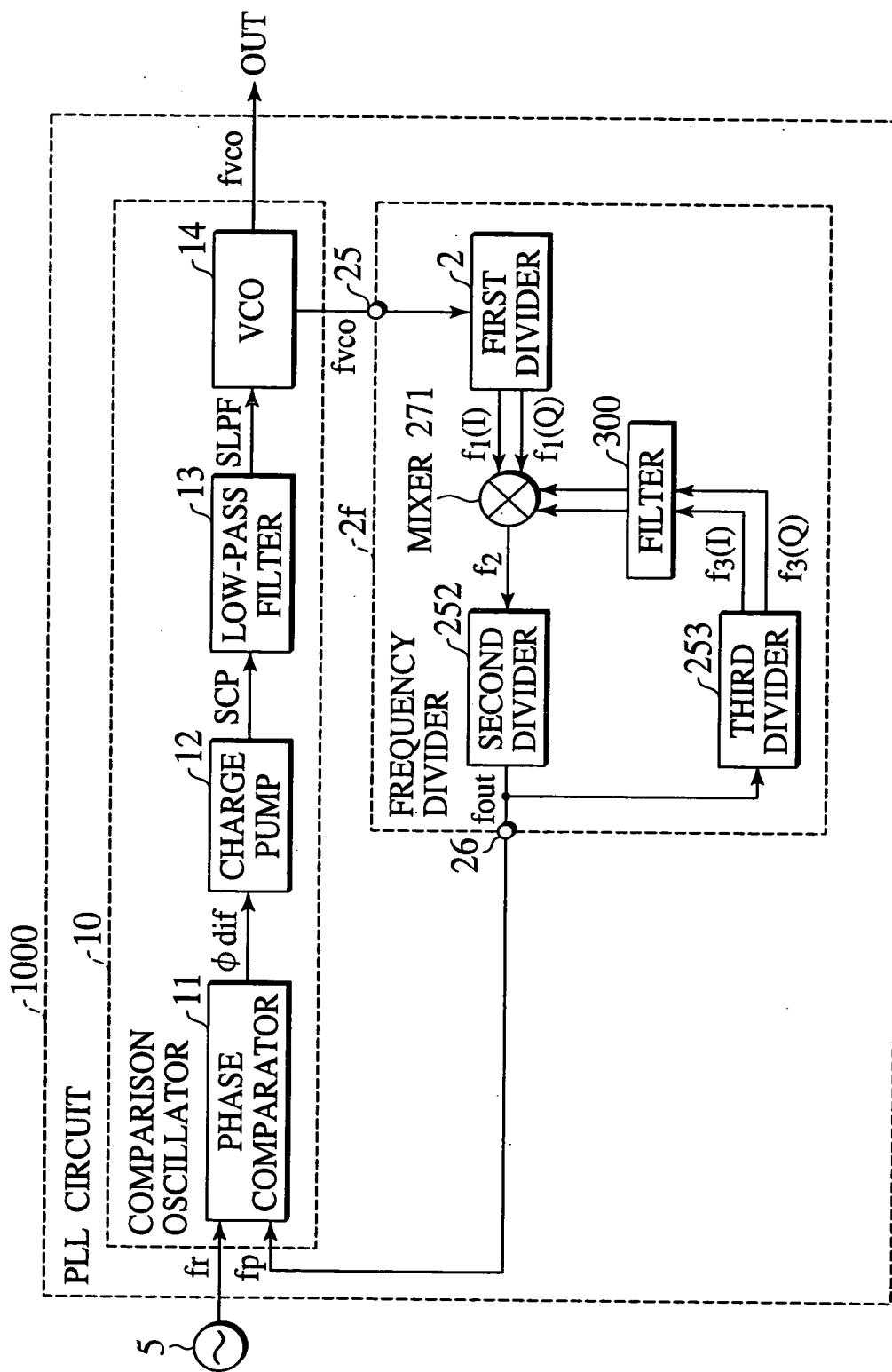


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FIG.11



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FIG.12

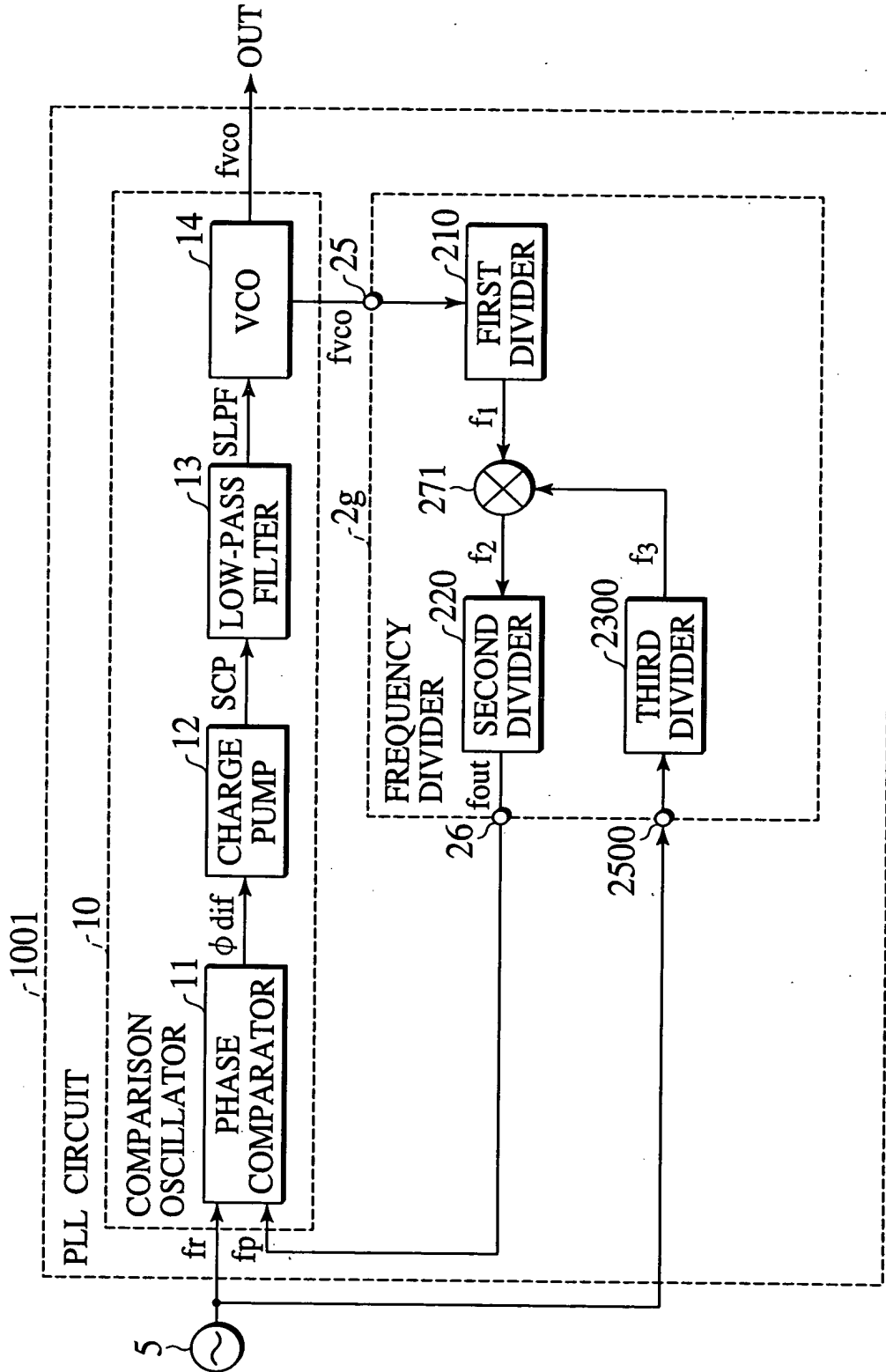
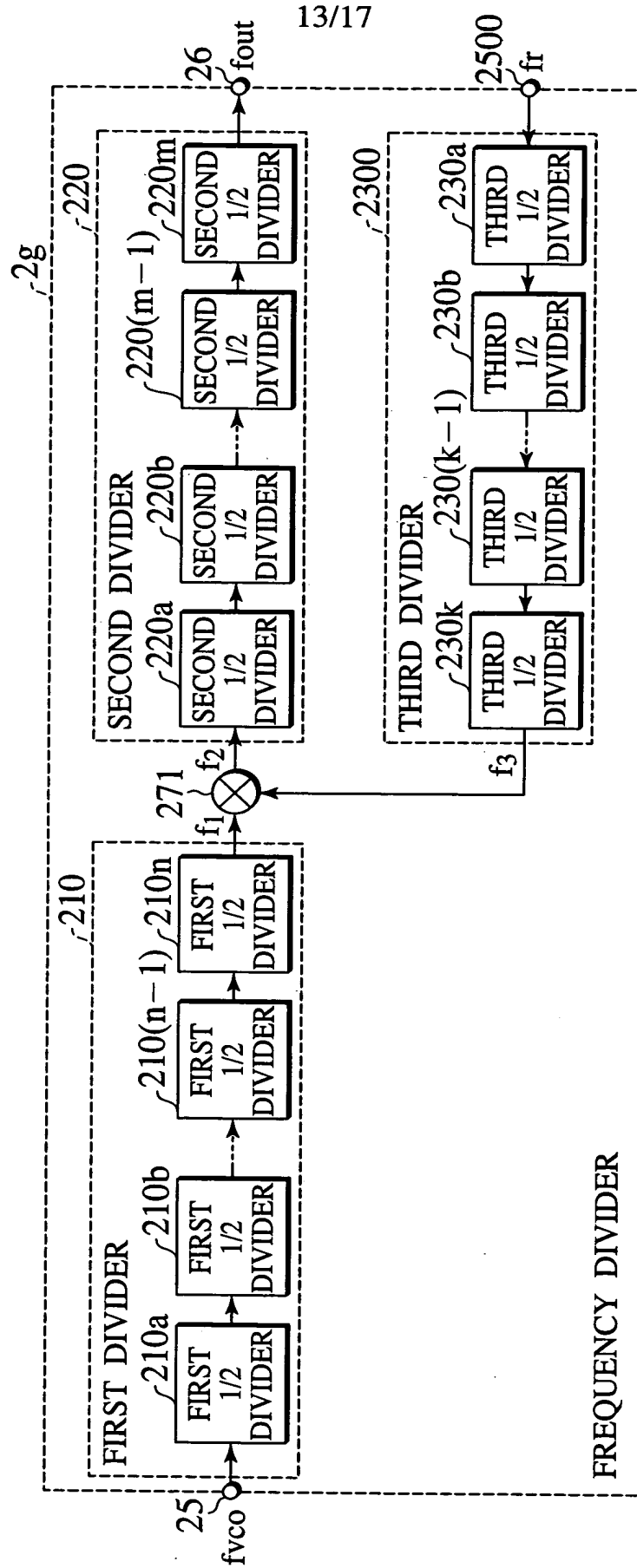
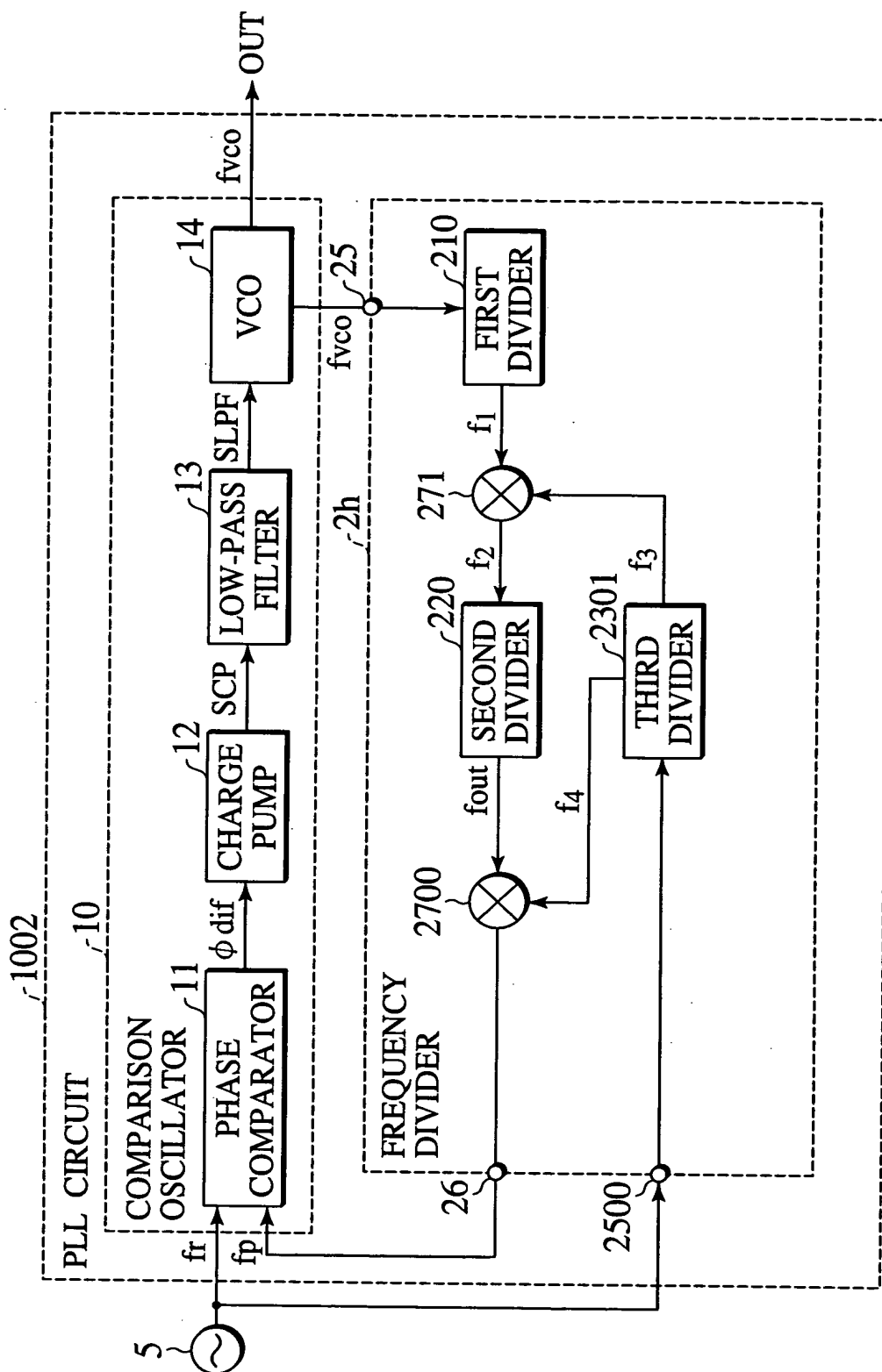


FIG.13



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FIG.14



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FIG.15

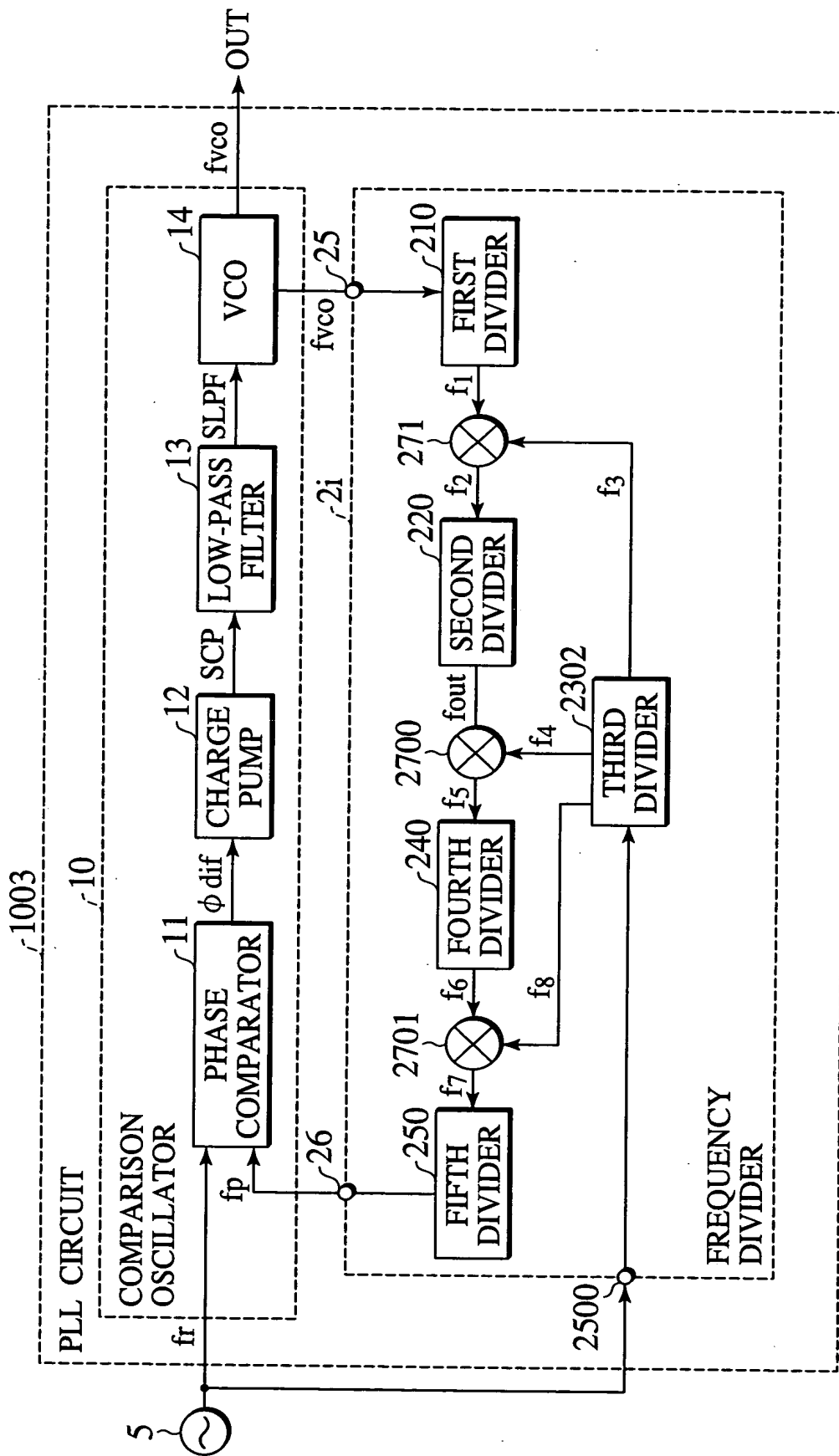
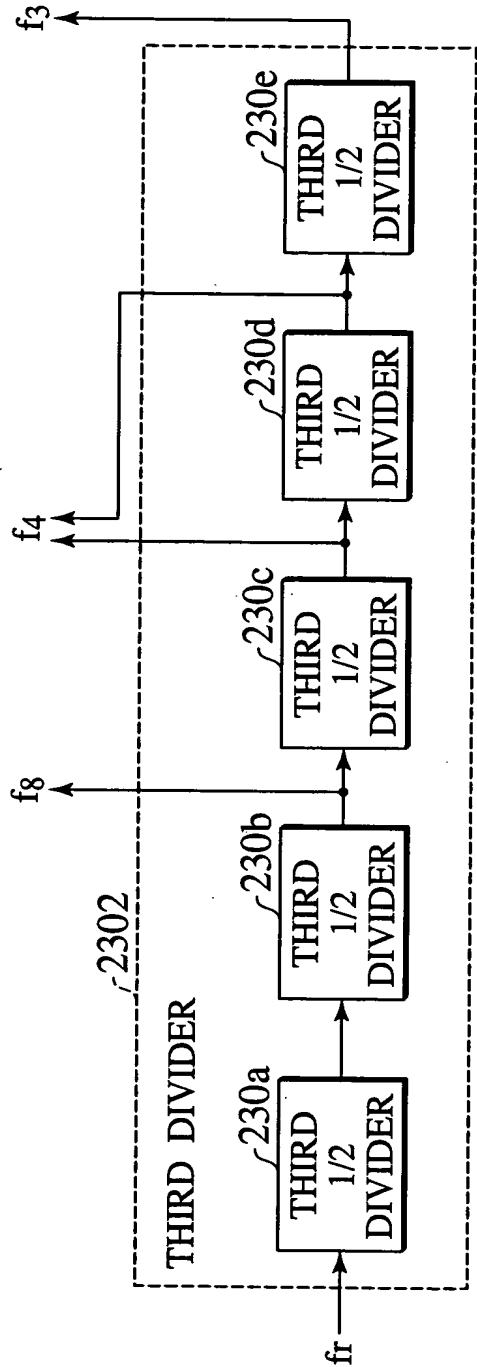


FIG.16





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FIG.17

